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Substitute for form 1449/PTO  <b>SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/791,686
				Filing Date	March 3, 2004
				First Named Inventor	David A. BAER
				Art Unit	2622
				Examiner Name	HSIA, SHERRIE Y.
Sheet	1	of	2	Attorney Docket Number	1875.3970001

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
	US1	4,502,078	02-26-1985	Steckler et al.	
	US2	4,623,926	11-18-1986	Sakamoto	
	US3	5,031,233	07-09-1991	Regan	
	US4	5,467,342	11-14-1995	Logston et al.	
	US5	5,570,137	10-29-1996	Goeckler	
	US6	5,715,012	02-03-1998	Patel et al.	
	US7	5,737,035	04-07-1998	Rotzoll	
	US8	5,847,612	12-08-1998	Birleson	
	US9	5,905,405	05-18-1999	Ishizawa	
	US10	5,987,078	11-16-1999	Kiyanagi et al.	
	US11	6,005,640	12-21-1999	Strolle et al.	
	US12	6,115,432	09-05-2000	Mishima et al.	
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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
	FP1					
	FP2					
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	FP4					
	FP5					
	FP6					
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	US21	6,559,898 B1	05-06-2003	Citta et al.	
	US22	6,636,270 B2	10-21-2003	Gates et al.	
	US23	6,680,955 B1	01-20-2004	Le	
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	US30	7,151,945 B2	12-19-2006	Myles et al.	
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	US32	7,397,822 B2	07-08-2008	Golan et al.	
	US33	2005/0039065 A1	02-17-2005	Cheung et al.	
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	US35				
	US36				
	US37				
	US38				
	US39				
	US40				

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NON PATENT LITERATURE DOCUMENTS			
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	NPL1	Poole, S., Surface, G., Singh, B., Dyer, N., "A CMOS Subscriber Line Audio Audio Processing Circuit Including Adaptive Balance," IEEE International Symposium on Circuits and Systems, Finland, Espoo, vol. 2 of 3, Jun. 7-9, 1988, pp. 1931-1934.	
	NPL2	A copy of a Non-Final Office Action dated August 23, 2006, issued in U.S. Patent Application No. 10/848,160, filed May 19, 2004.	
	NPL3	A copy of a Final Office Action dated May 21, 2007, issued in U.S. Patent Application No. 10/848,160, filed May 19, 2004.	
	NPL4	A copy of a Non-Final Office Action dated January 8, 2008, issued in U.S. Patent Application No. 10/848,160, filed May 19, 2004.	
	NPL5	JAFFE, Steven, "Digital IF modulator", U.S. Patent Application No. 12/076,849, filed March 24, 2008.	
	NPL6	A copy of a Final Office Action dated September 3, 2008, issued in U.S. Patent Application No. 10/640,648, filed May 19, 2004.	
	NPL7		
	NPL8		
	NPL9		
	NPL10		

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